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ATTENTION: Examiner Joshua D. Schneider  
Group Art Unit 2182

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FROM: George A. Herbster

SUBJECT: Serial No. 10/688,180, Filed October 17, 2003  
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re : NATAN VISHLITZKY, DOUGLAS E. LECRONE, IZHAR  
SHARON, DANIEL A. MURPHY, WILLIAM R.  
FAIRCHILD, HANA MORESHET, MARTIN FARLEY AND  
ELIZABETH C. PATAPOUTIAN

Serial No. : 10/688,180

Filed : October 17, 2003

FOR : METHOD FOR ENABLING OVERLAPPED INPUT/OUTPUT  
REQUESTS TO A LOGICAL DEVICE USING ASSIGNED  
AND PARALLEL ACCESS UNIT CONTROL BLOCKS

EXAMINER : Joshua D. Schneider

ART UNIT : 2182

September 13, 2004

Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being sent via facsimile (703-872-9306) to Group Art Unit 2182, Examiner Joshua D. Schneider on September 13, 2004.

*Mary C. Hoggard*

MARY C. HOGGARD

*Sept. 13, 2004*

September 13, 2004

Sir: